

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	78	((("6016554") or ("6034538") or ("6058263") or ("6075941") or ("6094730") or ("6107826") or ("6144327") or ("6161199") or ("6185522") or ("6202044") or ("6223144") or ("6223272") or ("6289300") or ("6298320") or ("6302268") or ("6347395") or ("6356862") or ("6374370") or ("6385742") or ("6460172") or ("6466898") or ("6487700") or ("6516428") or ("6564179") or ("6581191") or ("6718294") or ("6810442") or ("6816544") or ("6829727") or ("6922821") or ("6957180") or ("6967960") or ("7076420") or ("7089175") or ("7099818") or ("7162410") or ("7188063") or ("7236921"))).PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/17 10:06

L2	38	((("6016554") or ("6034538") or ("6058263") or ("6075941") or ("6094730") or ("6107826") or ("6144327") or ("6161199") or ("6185522") or ("6202044") or ("6223144") or ("6223272") or ("6289300") or ("6298320") or ("6302268") or ("6347395") or ("6356862") or ("6374370") or ("6385742") or ("6460172") or ("6466898") or ("6487700") or ("6516428") or ("6564179") or ("6581191") or ("6718294") or ("6810442") or ("6816544") or ("6829727") or ("6922821") or ("6957180") or ("6967960") or ("7076420") or ("7089175") or ("7099818") or ("7162410") or ("7188063") or ("7236921"))).PN.	USPAT	OR	OFF	2008/03/17 10:07
L3	1	("6032268").PN.	USPAT	OR	OFF	2008/03/17 10:19
L4	6	((("20020052729") or ("20020116168") or ("20020156885") or ("20020156998") or ("20030056071") or ("20030149961"))).PN.	US-PGPUB	OR	OFF	2008/03/17 11:28
L5	110	((processor cpu microprocessor microcontroller) and (emulator emulation simulator simulation) and (breakpoint halt debug)). clm.	US-PGPUB	OR	ON	2008/03/17 11:59

L6	14	((processor cpu microprocessor microcontroller) and (emulator emulation simulator simulation) and (breakpoint halt debug) and (lock\$step synchronous synchronized synchronization)).clm.	US-PGPUB	OR	ON	2008/03/17 11:59
L7	96	L5 not L6	US-PGPUB	OR	ON	2008/03/17 11:59
L8	22	((master and slave) same (microcontroller)) same (compar\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/17 11:59
L9	90	microcontroller and ((cache register memory) same compar\$5) and (lock \$step)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/17 11:59
L10	274	(cache same compar\$5) and (lock\$step)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/17 11:59

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